

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
22 February 2001 (22.02.2001)

PCT

(10) International Publication Number
WO 01/13231 A1

(51) International Patent Classification⁷: **G06F 11/00**

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(21) International Application Number: **PCT/FI00/00689**

(22) International Filing Date: **14 August 2000 (14.08.2000)**

(25) Filing Language: **English**

(26) Publication Language: **English**

(30) Priority Data:
19991735 **16 August 1999 (16.08.1999)** **FI**

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(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.

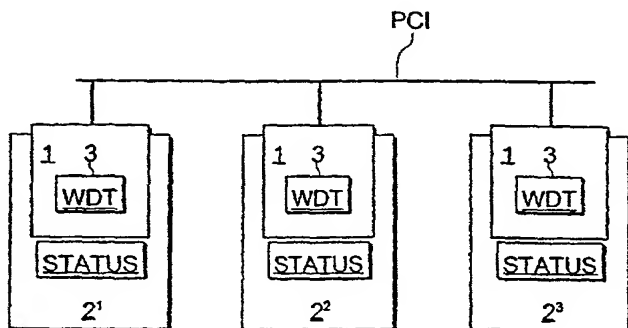
(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:

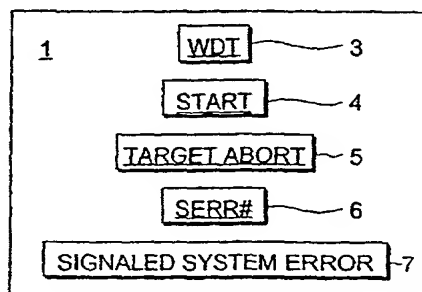
— With international search report.

[Continued on next page]

(54) Title: **METHOD AND DEVICE FOR IMPROVING THE RELIABILITY OF A COMPUTER SYSTEM**



(57) Abstract: The invention relates to a method and device for improving the reliability of a computer system by using a CompactPCI bus. The computer system comprises a bus (PCI), an interface circuit (1) and a plug-in unit (2) which is connected to the bus via the interface circuit (1). In the method, the interface circuit (1) is provided with a watchdog timer (3), which is started upon the start of addressing of the plug-in unit, and addressing is terminated if its duration exceeds a time value preset in the watchdog timer (3). The interface circuit (1) of the invention comprises a watchdog timer (3), means (4) for starting timing upon the start of addressing, and means (5) for terminating the addressing if its duration exceeds the time value preset in the watchdog timer (3).



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